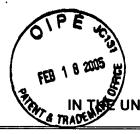
J. For.



**PATENT** 

## UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Takashi KOBAYASHI

Serial No.: 10/020,573

Filed: October 31, 2001

Title: VERTICAL INSULATED GATE SEMICONDUCTOR DEVICE

Group Art Unit: 2826

Examiner: Nathan Flynn

Attorney Docket No.: FUJI:200

**COMMISSIONER FOR PATENTS** 

P.O. Box 1450

ALEXANDRIA, VA 22313-1450

MS: ISSUE FEE

1450, Alexandria, VA 22313-1450 on:

Certificate of Mailing
I hereby certify that this paper is being deposited with the

United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box

Date:

By: Marc A Ros

## RESPONSE TO NOTICE REGARDING DRAWINGS

Sirs:

Please substitute the attached one sheet of formal drawing 24/34 depicting Figure 28 for the drawings filed on March 11, 2002.

Respectfully submitted,

ROSSI, KIMMS & McDOWELL LLP

Marc A. Rossi Reg. No. 31,923

P.O. BOX 826 Ashburn, VA 20146-0826 703-726-6020 (Phone) 703-726-6024 (fax)

0 L/15/04